



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Gurupada Mandal

Assignee: Sun Microsystems, Inc.

Title: BITLINE BOOSTER CIRCUIT AND METHOD

Serial No.: 10/769,130 Filing Date: January 29, 2004

Examiner: Unassigned Group Art Unit: 2811

Docket No.: SUN030155

Monterey, CA
November 12, 2004

Mail Stop Missing Parts
Commissioner for Patents
P.O. Box 1450
Alexandria VA 22313-1450

PETITION FOR EXTENSION OF TIME

Sir:

Applicant respectfully petitions for a three-month extension of time within which to respond to the outstanding Notice to File Corrected Application Papers dated June 15, 2004. This petition extends the time for response until November 15, 2004.

Enclosed herewith is a check in the amount of \$980.00 payable to the Director of the United States Patent and Trademark Office, which is the amount for a three-month extension of time given in 37 C.F.R. §1.17(a)(3).

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Mail Stop Missing Parts, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on November 12, 2004, 2004.

GUNNISON, MCKAY &
HODGSON, LLP.
Garden West Office Plaza, Suite 220
1900 Garden Road
Monterey, CA 93940
(831) 655-0880
Fax (831) 655-0888

Philip J. McKay

Attorney for Applicant

November 12, 2004

Date of Signature

Respectfully submitted,

Philip J. McKay
Attorney for Applicant
Reg. No. 38,966
(831) 655-0880

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